Silicon neurons that burst when primed

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Abstract—We present a silicon neuron that responds with a high-frequency burst of spikes (> 250Hz) when appropriately primed; otherwise it responds with lower frequency single spikes (< 100Hz). Our model is based upon neurons that relay sensory inputs to the cortex. The bursts (2-6 spikes) are caused by an excitatory calcium channel, designed using circuits that capture the channel's nonlinear dynamics. With sufficient priming, the channel opens quickly and closes slowly when the neuron's voltage increases. Priming the channel requires lowering the neuron's voltage for a sufficient period; thus, inhibitory inputs that can control the cell's response mode. We fabricated 7200 relay neurons on a 11.5mm² chip in 0.25 μ m CMOS. We plan to use feedback from a cortex chip to prime the relay neurons and thereby realize attention.

I. BURSTING FOR ATTENTION

Our senses provide a great deal of information about the world to our brain, more than it can handle at any one time. The brain deals with this deluge with attention—a means to suppress irrelevant inputs as well as enhance relevant ones, essentially increasing their saliency. This can be achieved at the cellular level if the cell's response depends on its state.

While not possible with simple integrate-and-fire neurons, neurons with nonlinear ion channels—such as Hodgkin-Huxley channels, which are easily modeled with subthreshold transistor circuits [2]—can have different response states. For example, a cell may respond to salient input with a burst of spikes, but respond to background noise with only single-spikes. Bursts not only increase the strength of a signal, they allow a cortical cell to filter out the thousands of single spikes it receives in its dendrites by applying a threshold to each one. Indeed, researchers have observed that bursts are better at activating widespread recurrent activity within the cortex [1].

The thalamus is ideally situated to mediate bursting, as (most) sensory information en route to the cortex passes through it. In fact, thalamic cells display two dramatically different responses, either individual spikes (tonic response) or high-frequency spike bursts (burst response). Responsible for this ability is an excitatory calcium channel—called a T-channel—which opens quickly (activation) when the cell's voltage increases, initiating a burst, and then closes slowly (inactivation), ending the barrage of spikes. However, the voltage must remain low for some minimum duration (priming), otherwise a burst fails to occur when the voltage increases (trigger).

Thalamic relay cells could be primed to burst by feedback axons from the cortex to the thalamus, which pass through a thin layer of cells surrounding the thalamus called the reticular nucleus. These reticular cells are inhibitory, and project their axons into the same thalamic area as the cortical axons, providing a means to prime the T-channels.

In this paper, we present a silicon T-channel circuit and attach it to a silicon neuron to model the thalamic relay cell. We use a previously published design to model channel activation, and a novel design to model inactivation. Using step and sinusoidal input currents, we demonstrate that sufficient inhibition primes the silicon T-channel, which then elicits a burst of spikes when triggered by excitatory

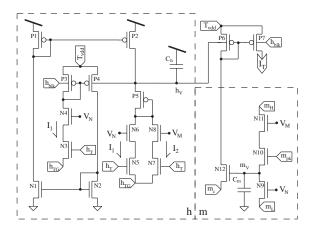


Fig. 1. This circuit's output current I_T corresponds to the fraction of T-channels that are open. To account for the processes of activation and inactivation, this fraction is the product of two variables m and h, logarithmically related to the voltages m_V and h_V . The rates of activation and deactivation are represented by the currents in N11 and N9, while rates of inactivation and deinactivation are represented by the currents I_2 and I_1 , respectively. These currents are determined by the membrane voltage (V_M) and a voltage that is linearly related (V_N) , but with negative slope. Bias voltages m_H and m_L scale the rates and shift the halfway point for activation; h_T and V_N 's offset play similar roles for inactivation.

input. In the absence of inhibition, the cell switches from burst-mode to tonic-mode. Thus, inhibitory inputs control the cell's firing mode.

II. SILICON T-CHANNEL

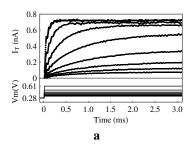
Our T-channel circuit (Fig. 1) derives from our previous work on linear thermodynamic models of voltage-dependent ion channels [2]. Individual channels are modeled as a series of gates, all of which must be open for conduction to occur. A *channel variable* represents the fraction of open gates within the population, and the product of all the channel variables represents the fraction of channels in the membrane that are conducting.

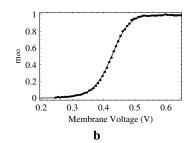
A variable's dynamics is described by:

$$\dot{u}\left(t\right) = -\frac{1}{\tau\left(V\right)}\left(u\left(t\right) - u_{\infty}\left(V\right)\right) \tag{1}$$

where $u_{\infty}(V)$ is its steady-state and $\tau(V)$ its time-constant; they have sigmoid and bell-shaped voltage-dependencies, respectively.

The T-channel circuit has two channel variables: one for activation (m) and one for inactivation (h). Activation variables increase as the membrane voltage increases—their steady-state sigmoid reaches a maximum at higher voltages; the opposite relationship holds for inactivation variables. In our circuit, m and h are logarithmically related to the voltages m_V and h_V , respectively. We use the current mirror (P6-7) to take their product, which yields





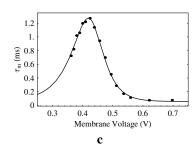
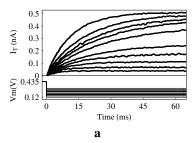
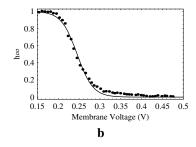


Fig. 2. Activation variable m. a, Dynamics of m as measured through I_T, in response to step changes of the membrane voltage V_M (shown at bottom). $V_{
m M}$ begins low so that m=0, and steps to higher voltages. b, Voltage-dependence of m's steady-state, as measured through $I_{
m T}$ by sweeping $V_{
m M}$ slowly (normalized). The fit is to Eqn. 3. c, Voltage-dependence of m's time-constant, extracted by fitting the measurements in a to the solution of Eqn. 1 for step changes in V. The fit is to Eqn. 4.





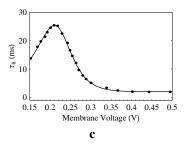


Fig. 3. Inactivation variable h. a, Dynamics of h as measured through I_T, in response to step changes of the membrane voltage V_M (shown at bottom). b, Voltage-dependence of h's steady-state, as measured through I_T by sweeping V_M slowly (normalized). The fit is to Eqn. 3. c, Voltage-dependence of h's time-constant. We extracted the time-constant from a, obtained as in Fig. 2c. The fit is to Eqn. 4.

$$I_{\rm T} = I_{\rm ds0} e^{\kappa \, m_{\rm V} - m_{\rm C}} e^{(1-\kappa)(h_{\rm V} - h_{\rm blk})}$$

= $m^{\kappa} h \, \bar{I}_{\rm T}$, (2)

where $m = e^{m_V - m_H}$, $h = e^{(1-\kappa)(h_V - h_{blk})}$, and $\overline{I}_T =$ $I_{\rm ds0}~e^{\kappa~m_{\rm H}-m_{\rm C}}$ (the maximum T-channel current). Note that all voltages here, and in further equations, are in units of the thermal voltage U_T. We describe the activation and inactivation subcircuits next.

A. Activation Variable m

For our activation subcircuit, we use a previously published design [2], which requires only three transistors to achieve the steady-state's and time-constant's voltage-dependencies. N11's current represents the activation rate, which increases when V_M—the dendritic membrane voltage-increases. N9's current represents the deactivation rate, which increases with V_N—the inverted dendritic membrane voltage, produced by a negative-gain amplifier. Thus, when $V_{\rm M}$ increases, $V_{\rm N}$ decreases, and $m_{\rm V}$ moves towards $m_{\rm H}$, increasing m. When $V_{\rm M}$ decreases, $V_{\rm N}$ increases, and $m_{\rm V}$ moves toward $m_{\rm L}$, decreasing m. The time-constant is inversely related to the sum of the rates; we use mPK to limit the magnitude of the N11's current, fixing the minimum time-constant at large membrane voltages.

The steady-state's and time-constant's voltage-dependencies, derived using the subthreshold MOS transistor equations [2], are:

$$m_{\infty} \left(V_{M} \right) = \frac{1}{1 + \exp \left[-\frac{V_{M} - V_{m}^{mid}}{V_{m}^{*}} \right]}$$
(3)

$$m_{\infty} (V_{M}) = \frac{1}{1 + \exp\left[-\frac{V_{M} - V_{m}^{mid}}{V_{m}^{*}}\right]}$$

$$\frac{\tau_{m} (V_{M})}{\hat{\tau}_{m}} = 1 + \frac{1}{\exp\left(\frac{V_{M} - V_{1m}}{V_{1m}^{*}}\right) + \exp\left(-\frac{V_{M} - V_{2m}}{V_{2m}^{*}}\right)}$$
(4)

where $\hat{\tau}_{\rm m}=(C_{\rm M}\,U_{\rm T}/I_{\rm ds0})\,e^{m_{\rm H}-\kappa\,m_{\rm PK}}$ is the time-constant at high and low membrane voltages; $V_{\rm m}^{\rm mid},\,V_{\rm 1m},\,V_{\rm 2m}$ are functions of the voltage biases m_H , m_L , m_{PK} and n_{VN} (see Fig. 4); and V_m^* , V_{1m}^* , V_{2m}^* are determined by the transistor parameters κ and U_T (see [2] for more details).

Since $I_T = m^{\kappa} h \overline{I}_T$, we can record the dynamics of m by fixing the inactivation variable h and measuring I_T , stepping V_M to increasingly higher voltages (Fig. 2a). We notice two effects: The steady-state increases with the step-size, and the rate at which the output reaches steady-state changes, slowing down initially, but then speeding back up again. We extract the time-constant's voltage-dependence from these curves, and measure the steady-state's voltage-dependence by sweeping V_M slowly and recording I_T. Our experimental results are in good agreement with Eqns. 3 and 4 (Fig. 2b and Fig. 2c).

B. Inactivation Variable h

The inactivation subcircuit's operation is similar in principle to the activation subcircuit's. Whereas the activation subcircuit operates by modulating N10's source (m_V) to match its current with N9's, the activation subcircuit operates by modulating P4's well (h_V) to match the currents in P2 and P5. We use the PMOS's back-gate to achieve longer time-constants, as the back-gate's voltage has to change more than the gate's due to the former's weaker influence on current. This is equivalent to increasing the capacitor Ch, but is more efficient in surface area.

The differential equation describing h_V is:

$$C_h \frac{dh_V}{dt} = I_2 + I_1 - I_1 e^{-(1-\kappa)(h_V - h_{blk})},$$

where the gain on the last term comes from the mirror P3-4. Recall that $h={\rm e}^{(1-\kappa)({\rm hv-h_{blk}})}$, therefore

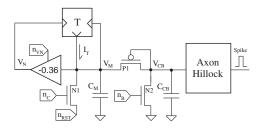


Fig. 4. Our neuron circuit is a two-compartment model of the relay cell: C_M represents the dendrite, and C_{CB} represents the cell body. The T-channel (T) is desribed in Fig. 1. The Axon Hillock generates a spike once V_{CB} reaches a fixed threshold, and then resets V_{CB} to Gnd. The amplifier inverts V_M with a gain of $-0.36;\,V_N=n_{VN}$, an adjustable bias, when $V_M=0$ (Gnd).

$$Q_{h} \frac{1}{h} \frac{dh}{dt} = -\left(\frac{1}{h} - 1\right) I_{1} + I_{2}$$

$$\frac{dh}{dt} = -\frac{I_{1} + I_{2}}{Q_{h}} \left(h - \frac{I_{1}}{I_{1} + I_{2}}\right)$$
(5)

where $Q_h = C_h U_T/(1-\kappa)$. Eqn. 5 is now equivalent to Eqn. 1, as both I_1 and I_2 are functions of V_M . Through algebraic manipulation of transistor equations [3], we can show the h's steady-state and time-constant have the same form as Eqns. 3 and 4, except $\hat{\tau}_h = (C_h U_T/I_{ds0}) \; e^{h_{TG}-\kappa \, h_T}/(1-\kappa)$; and $V_h^{mid}, \, V_{1h}, \, V_{2h}$ are determined by h_T and n_{VN} ; V_h^* , V_{1h}^* , and V_{2h}^* are determined by κ and U_T . The factor $(1-\kappa)$ in $\hat{\tau}_h$ derives from the use of the back-gate.

Like the activation variable, we can record the dynamics of h through I_T by fixing m (Fig. 3a). Stepping V_M to increasingly lower voltages, we notice an increase in the steady-state and a non-monotonic change in time-constant. These measured voltage-dependencies agree well with Eqns. 3 and 4 (Fig. 3b and Fig. 3c).

III. SILICON RELAY CELL

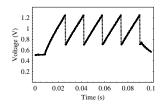
In this section, we attach our T-channel circuit to a silicon neuron, and use injected currents to probe the channel's influence on the cell's response. After describing the relay neuron circuit, we demonstrate the two response modes—tonic and burst—with input current steps. However, the T-channel can produce a graded response between these two extremes. I_T 's current amplitude depends on the degree of priming—the duration and magnitude of pre-burst inhibition. We demonstrate this using sinusoidal input currents of varying frequency and mean level.

We fabricated 7200 relay neurons on a 11.5mm² chip in $0.25\mu m$ CMOS, each neuron approximately 774 μm^2 in area. Surrounding the neural core is digital circuitry that controls action potential communication into, and out of, the network. The chip possesses a total of 110 I/O pads.

A. Cell Circuit

We use a two-compartment model for our cell (Fig. 4): One compartment represents the dendrite, the other, the cell body. The dendritic compartment has a membrane capacitor $(C_{\rm M})$ and a leak (N1). The T-channel's current $I_{\rm T}$ drives $V_{\rm M}$. Absent any input, the leak causes $V_{\rm M}$ to rest at $n_{\rm RST}.$ When the net input current is negative, $V_{\rm M}$ drops until N1's current matches the input. When the net input current is positive, $V_{\rm M}$ rises until P1 turns on and passes current into the cell body compartment.

The cell body compartment has three components: a membrane capacitor ($C_{\rm CB}$), a leak transistor (N2), and an axon-hillock circuit.



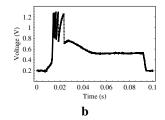


Fig. 5. Dendritic voltage (V_M) records of the relay cell's two response modes: a Tonic; b Burst. In tonic-mode, V_M is initially high $(\approx 500 \mathrm{mV}),$ which inactivates $I_T.$ In burst-mode, V_M is initially low $(\approx 200 \mathrm{mV}),$ which deinactivates $I_T.$ An $80 \mathrm{ms}$ -wide current step is injected into the dendritic compartment at $10 \mathrm{ms}$ in both cases.

Current from the dendritic compartment integrates on $C_{\rm CB}$. Once $V_{\rm CB}$ surpasses a voltage threshold, the axon hillock circuit—a modified version of the one in [4]—generates a spike. A reset transistor within the Axon Hillock then turns on, pulling $V_{\rm CB}$ down to Gnd, and then the cell starts to integrate current from the dendritic compartment again. We connect the source of the leak transistor to Gnd rather than a voltage bias (such as $n_{\rm RST}$ in the dendrites) because there are no inhibitory dynamics within the cell body, allowing us to use the whole voltage range from Gnd to threshold during input integration. The reset current within the cell body also affects $V_{\rm M}$ through P1. However, rather than reset to Gnd, $V_{\rm M}$ only resets partially due to the voltage-drop across P1 (see Fig. 5a).

B. Step Current Response

We first show the cell's response to a step current in the dendritic compartment (Fig. 5a). With no input, $V_{\rm M}\approx 500{\rm mV}$, sufficiently high to inactivate the T-channel. After the step, $V_{\rm M}$ begins to rise, and current eventually passes into the somatic compartment ($C_{\rm CB}$). $V_{\rm M}$ increases linearly until $V_{\rm CB}$ reaches spike threshold, and an output spike is generated (not shown). $V_{\rm CB}$ resets to Gnd, and $V_{\rm M}$, through the action of transistor P1, resets to approximately 700mV. After reset, dendritic integration continues. Since $V_{\rm M}$'s trajectory is linear, the firing rate increases linearly with input current (data not shown), as observed in real relay cells [5].

Next, we repeat the procedure with a constant inhibitory current added to the injected current step (Fig. 5b). The inhibitory current lowers $V_{\rm M}$ to approximately $200{\rm mV}$, thereby deinactivating $I_{\rm T}$ (i.e., h increases). After the step, $V_{\rm M}$ begins to rise; at around $400{\rm mV}$, $I_{\rm T}$ turns on, causing a rapid rise in $V_{\rm M}$ and initiating a burst. The initial spike rate approaches 1kHz, decreasing with each successive spike as h decreases, and eventually the cell stops firing.

Since the cell stops spiking once T-channel inactivates, we know that the cell would not spike absent $I_{\rm T}.$ This demonstrates that without the channel (i.e, in tonic-mode), the cell's response is completely dependent on the input, firing spikes at a rate that is a function of the input. With the channel, (i.e., in burst-mode), the cell's response no longer reflects the magnitude of the input; rather, the cell's response now reflects the occurence of an event, which in this case was the input current step.

C. Sinusoidal Response

We use sinusoidal currents to demonstrate I_T 's graded behavior. We begin with a 4Hz input current, selecting the response mode by using two different mean current levels. In tonic-mode, the cell spikes during the sinusoid's upward half (Fig. 6a). In burst-mode, the cell bursts during the sinusoid's rising phase (Fig. 6b). These responses are similar to those elicited by the step.

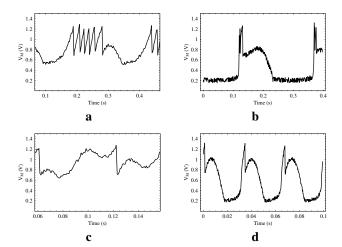


Fig. 6. Dendritic voltage $(V_{\rm M})$ records of the relay cell's response in tonic-mode $({\bf a,\,c})$ and burst-mode $({\bf b,d})$ to 4Hz $({\bf a,b})$ and 30Hz $({\bf b,\,d})$ sinusoidal input currents. The burst and tonic responses for each frequency have been aligned by phase.

At 30Hz, the cell responds subharmonically in tonic-mode but still phase-locks in burst-mode. With the high-mean input, which inactivates $I_{\rm T}$, the cell integrates over multiple cycles and fires at a random phase (Fig. 6c). With the low-mean input, which deinactivates $I_{\rm T}$, the cell manages to respond during each cycle (Fig. 6d); however, it fires only one spike (instead of a burst) since $V_{\rm M}$ spends less time low, preventing the T-channel from deinactivating fully.

For a series of frequencies, we observe no filtering in tonic-mode and bandpass filtering in burst-mode (Fig. 7a). In tonic-mode (blue), the cell maintains a (relatively) constant level of output activity, from multiple spikes per cycle at low frequencies to subharmonic spikes at high frequencies. In burst-mode (black), the number of spikes per cycle remains relatively constant at low frequencies, since the channel deinactivates fully each cycle, and so the average firing rate increases as the period shortens. As the frequency increases, the duration that $V_{\rm M}$ is low becomes shorter, and eventually the T-channel no longer deinactivates fully. This causes the number of spikes to decrease per cycle, as we demonstrated in Fig. 6d. Eventually, the T-channel cannot deinactivate, and the cell stops firing.

We observe a gradual transition from bandpass to allpass as we increase the mean current level. Two intermediate states are shown in Fig. 7a (red and green); they show characteristics of both firing modes. As the mean input current increases, $V_{\rm M}$ does not drop as much during the sinusoid's troughs, and so $I_{\rm T}$ does not deinactivate as much (see Fig. 3b for its voltage-dependence). The diminished burst response is evident in the bandpass' reduced peak. Conversely, the higher mean produces larger tonic responses at low frequencies due to the larger currents at the sinusoid's peaks.

With respect to phase, burst-mode leads while tonic-mode lags (Fig. 7b). The burst-mode's lead arises because the T-channel induces a burst before the sinusoid peaks. At high frequencies, the burst lags because there is a fixed delay between triggering and spiking. The tonic-mode always lags, since it relies on integration of the input. At low frequencies, this integration lag is negligible; as the period shortens, the latency becomes significant, causing a greater phase lag.

The two intermediate states follow the phase of the dominant mode (burst or tonic). At the low frequencies, where tonic dominates, the phase of both intermediate states approach the tonic phase curve. As the input frequency increases, burst starts to dominate, and their

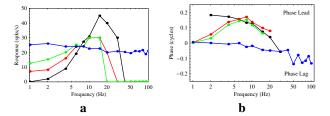


Fig. 7. Relay cell's frequency response for different firing modes: burst (black), tonic (blue) and burst-tonic (red and green). The response amplitude (a) and phase (b) are extracted from the Fourier transform of the output peristimulus time histogram (PSTH), each trial corresponding to a single cycle [6].

phases diverge from the tonic curve, approaching the burst curve.

IV. CONCLUSION

In this paper, we presented a silicon model of the thalamic relay cell. For the T-channel circuit, we used a previously published ion channel design for activation [2], but created a new circuit for inactivation that realizes its longer time-constants. We attached the T-channel circuit to a two-compartment silicon neuron. Using current inputs, we demonstrated the cell's different firing modes, and also the burst-mode's graded behavior.

For the cell to burst, its dendritic voltage must remain low for some duration to prime the T-channel. In the brain, surrounding the thalamus is a thin layer of inhibitory cells; they can drive the relay cells' voltages low and deinactivate the T current [7]. Corticothalamic feedback axons also excite these inhibitory cells, providing a means for cortex to control the thalamus' firing mode.

With a population of these silicon relay cells on our chip, we intend to study the influence of attention (i.e., cortical feedback) on sensory information passing through the thalamus. Evidence points to the involvement of both the thalamus [1] and the reticular nucleus [8] in attention. In particular, we plan to explore Francis Crick's "searchlight" hypothesis, which postulates that bursts are the expression of attention [9].

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